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**CONFIRMATION NO. 1625**

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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a DIV of 09/789,771 02/22/2001 PAT 6,701,613

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2000-263869 08/31/2000

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
Verified and Acknowledged <i>Kenji Iida</i>	Examiner's Signature <i>Kenji Iida</i>	Initials			

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**TITLE**

Multilayer circuit board and method of manufacturing the same

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